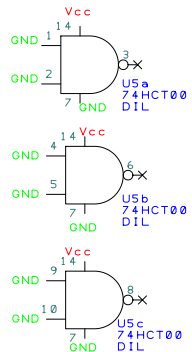
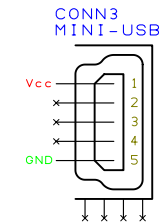
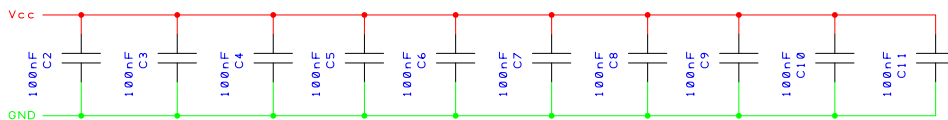


Spare gates



Decoupling capacitors



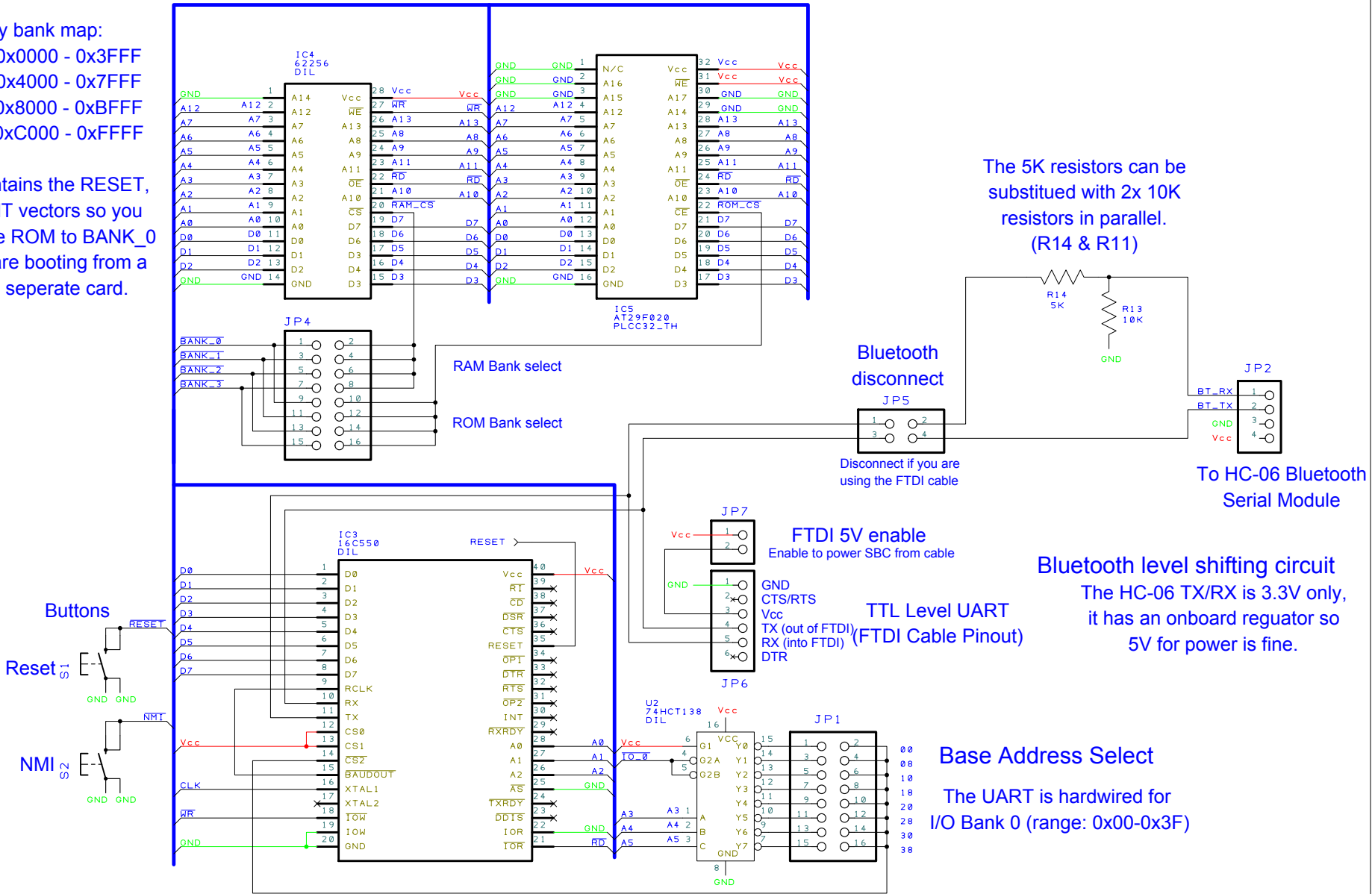
5V power only
for development when not
in the M62 Bus backplane

Only the first 16K of the RAM and ROM chips are used

Memory bank map:
 BANK_0 = 0x0000 - 0x3FFF
 BANK_1 = 0x4000 - 0x7FFF
 BANK_2 = 0x8000 - 0xBFFF
 BANK_3 = 0xC000 - 0xFFFF

BANK_0 contains the RESET, NMI, and INT vectors so you should set the ROM to BANK_0 unless you are booting from a ROM on a separate card.

The 5K resistors can be substituted with 2x 10K resistors in parallel. (R14 & R11)



Bluetooth disconnect
 JPS

Disconnect if you are using the FTDI cable

To HC-06 Bluetooth Serial Module

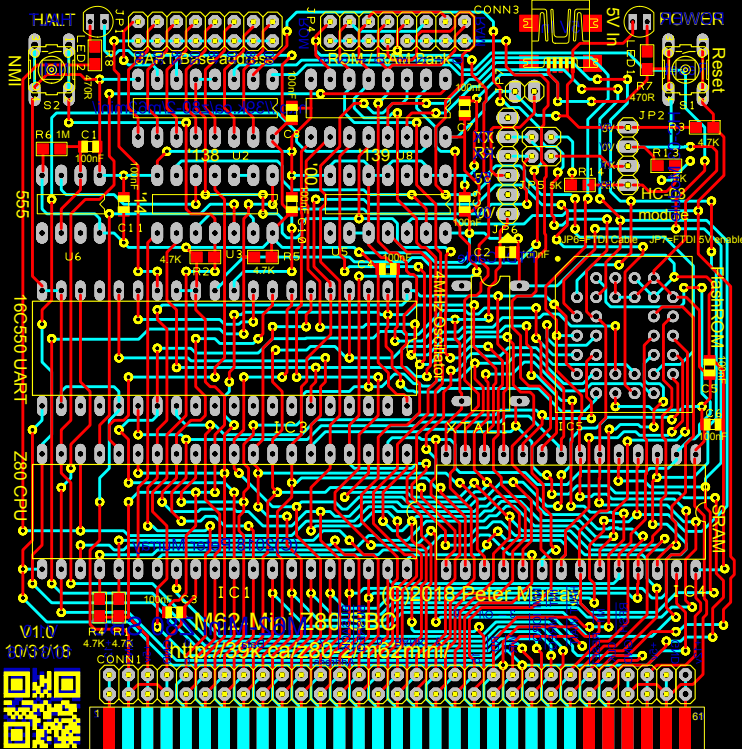
FTDI 5V enable
 Enable to power SBC from cable

TTL Level UART
 TX (out of FTDI)
 RX (into FTDI) (FTDI Cable Pinout)
 DTR

Bluetooth level shifting circuit
 The HC-06 TX/RX is 3.3V only, it has an onboard regulator so 5V for power is fine.

Base Address Select

The UART is hardwired for I/O Bank 0 (range: 0x00-0x3F)



HALT

CONN3

POWER

NMI

Reset

UART1 Base address

RAM Base address

555

16C550 UART

Z80 CPU

V1.0

10/31/18



CONN1

R4 R1
4.7K 4.7K

M62 Mini Z80 5BC

<http://39k.ca/z80/m62mini/>

Z80 2MHz Oscillator

IC2015 Peter M. J. 41



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