

M62 Interface

Bank decode

CPU

External clock input

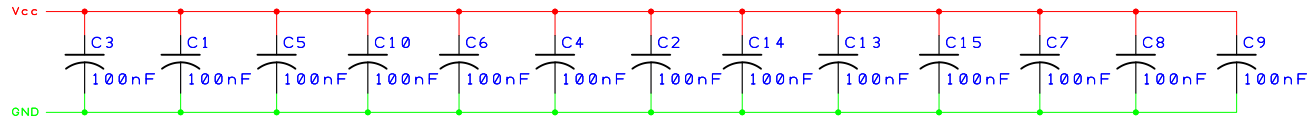
Clock

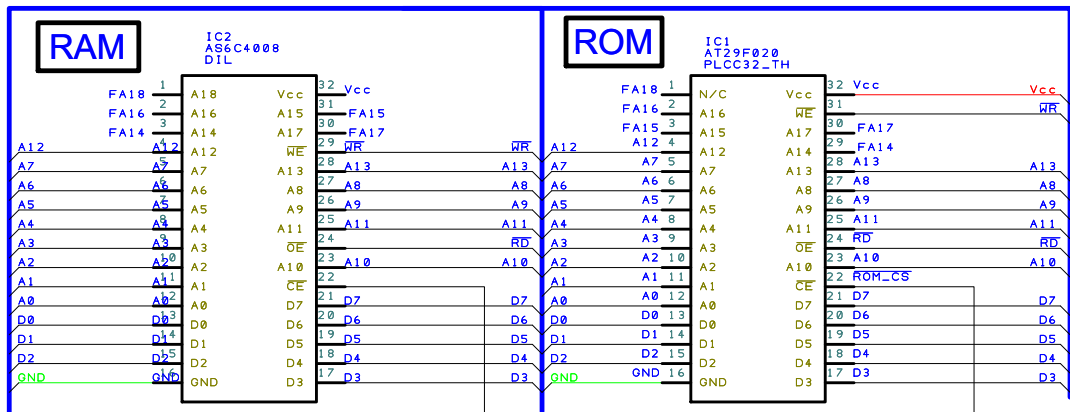
Power on reset circuit

Power LED

Halt LED

Decoupling capacitors



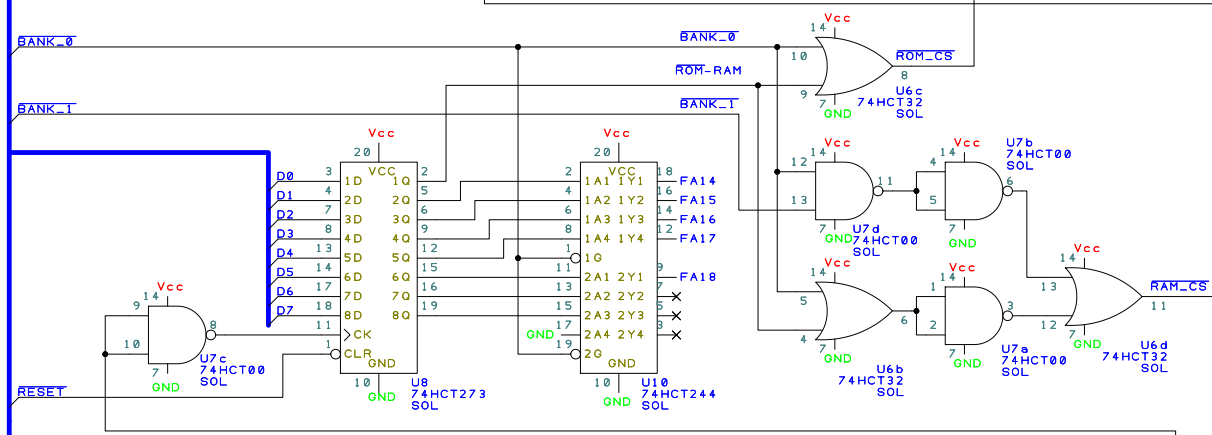
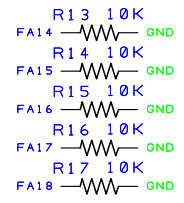


Bank 0 Configuration

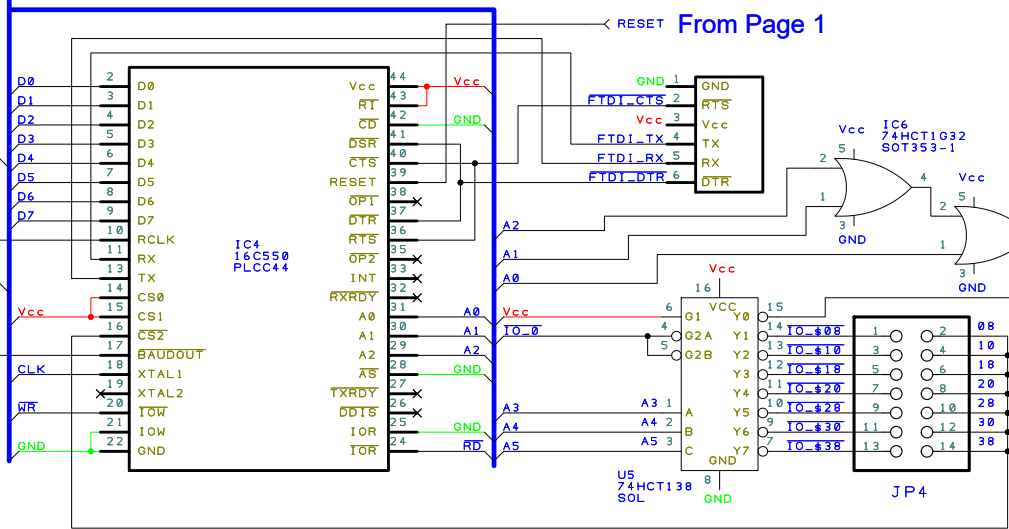
Writing to port \$00 sets Bank 0 function

Bit	Function
0	ROM/RAM
1	Address bit 14
2	Address bit 15
3	Address bit 16
4	Address bit 17
5	Address bit 18

Note: Defaults to \$00 on reset



Bank 0 Configuration logic



UART Circuit

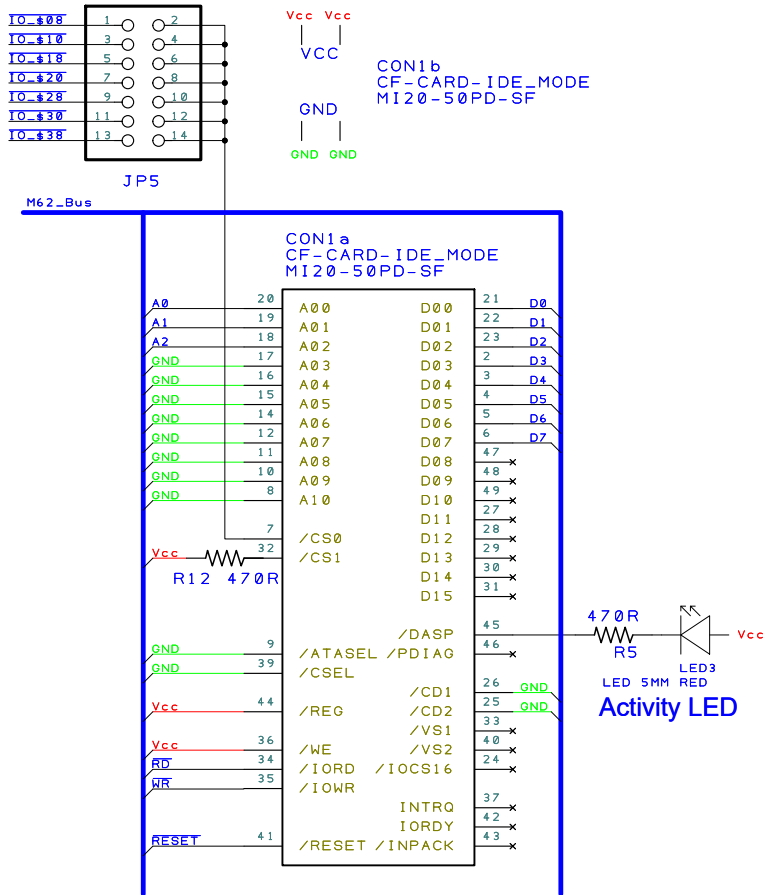
UART base Address Select

The UART is hardwired for I/O Bank 0 (range: 0x08-0x3F)
(0x00 is reserved for Bank 0 Configuration)

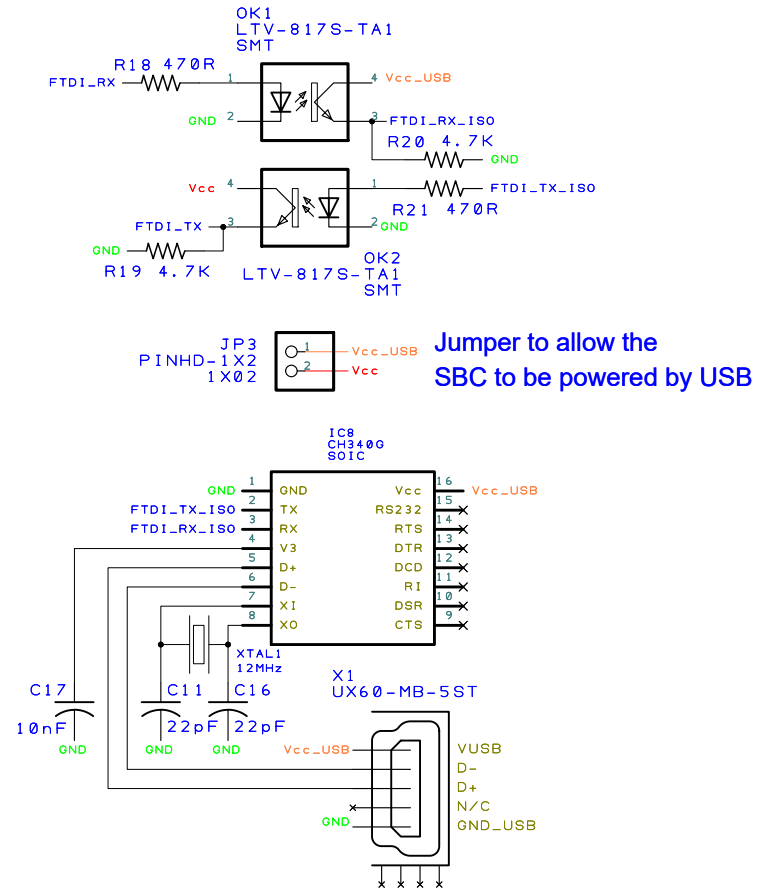
CF Base Address Select

The CF is hardwired for
I/O Bank 0 (range: 0x08-0x3F)
(0x00 is reserved for Bank 0 Configuration)

I/O lines from Page 2



CF Card interface



Isolated USB-UART interface